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Fig.1A

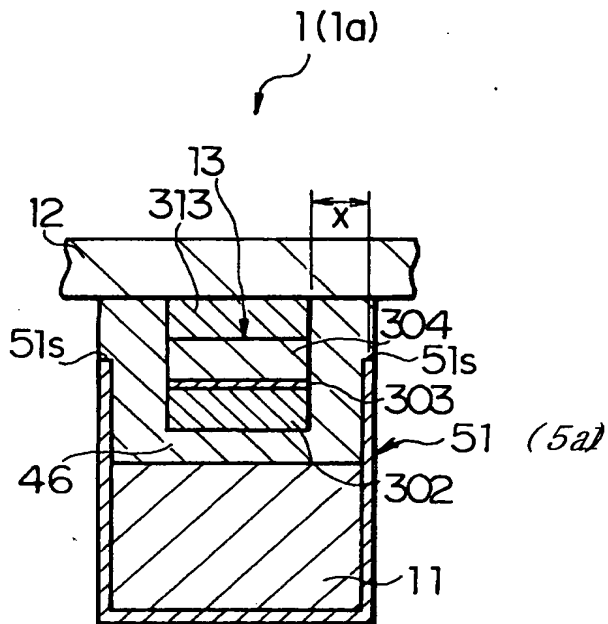
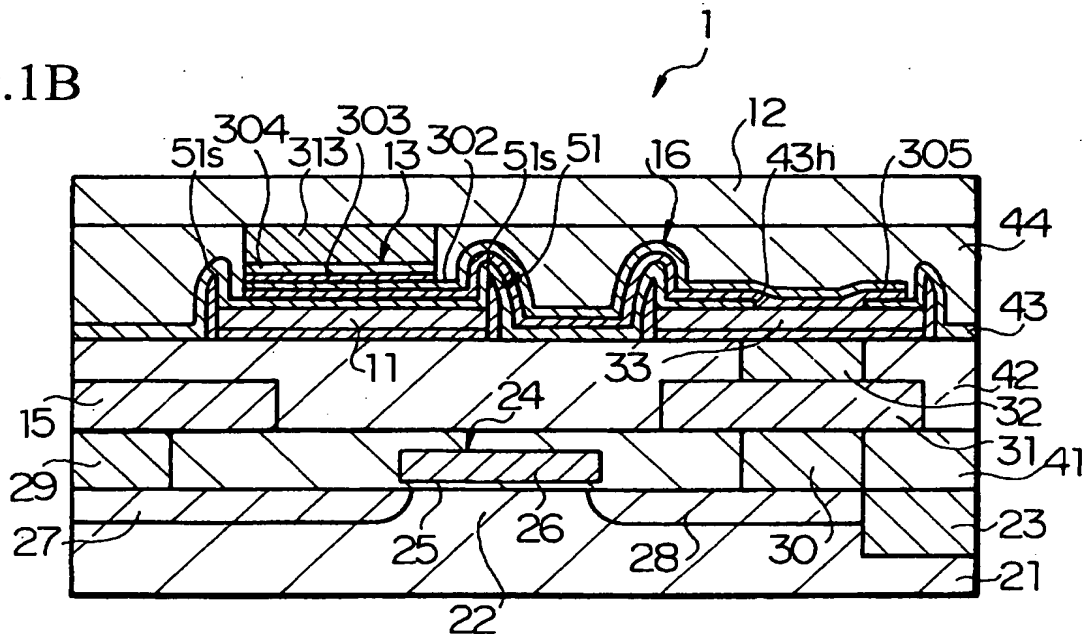


Fig.1B



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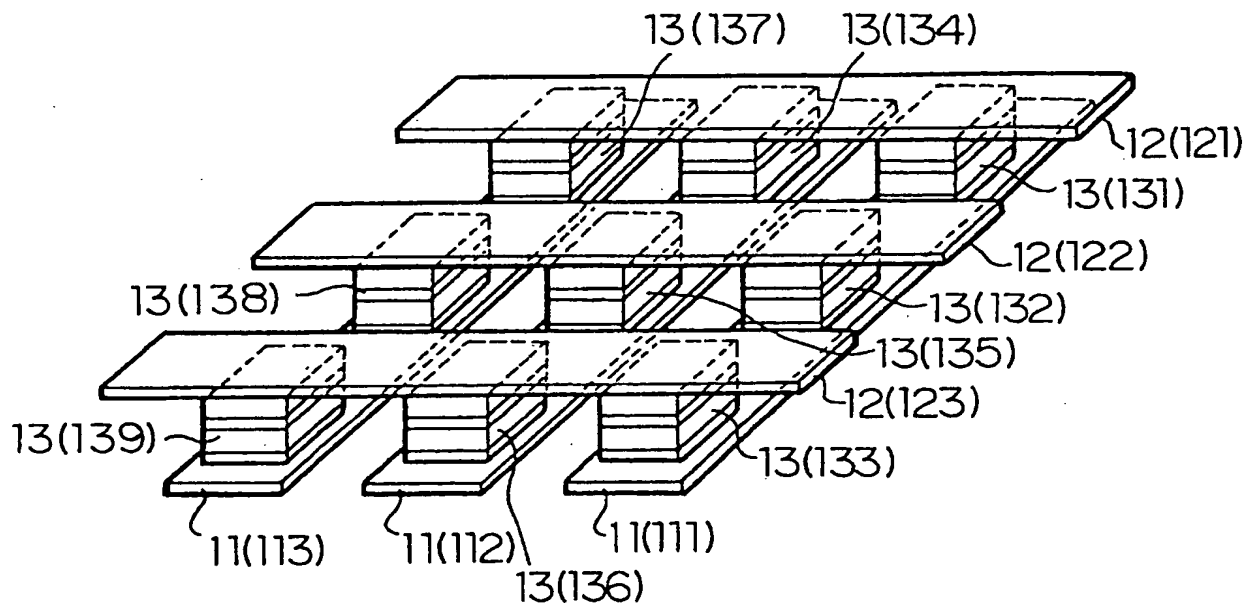


Fig.2

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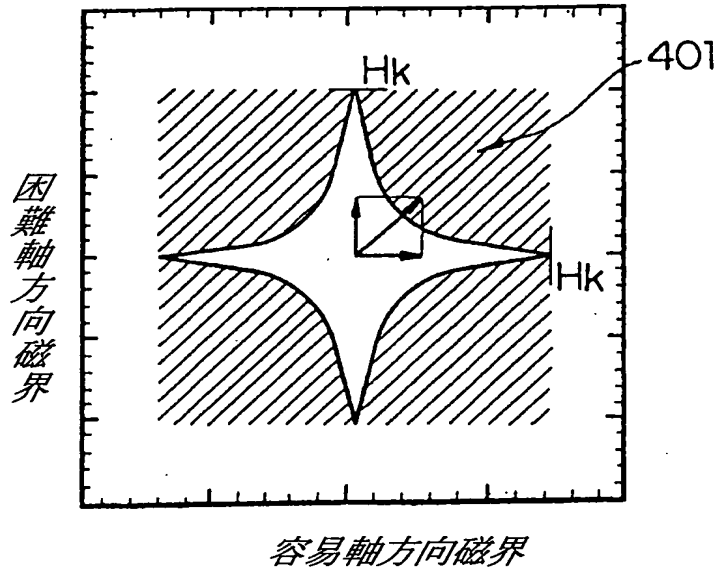


Fig.3

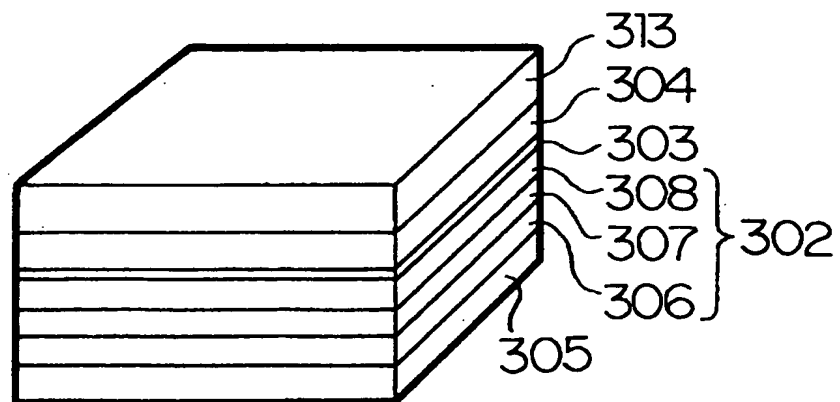


Fig.4

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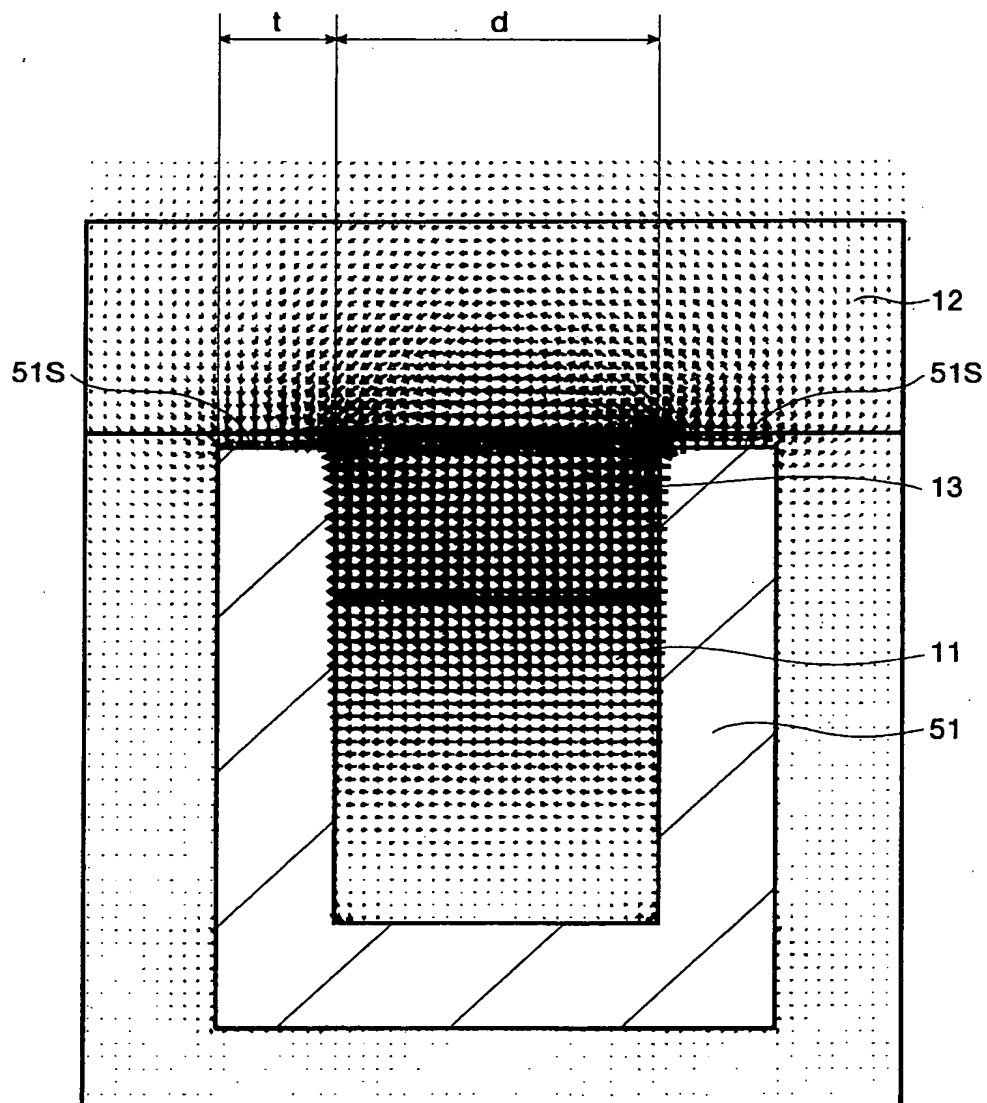


Fig.5

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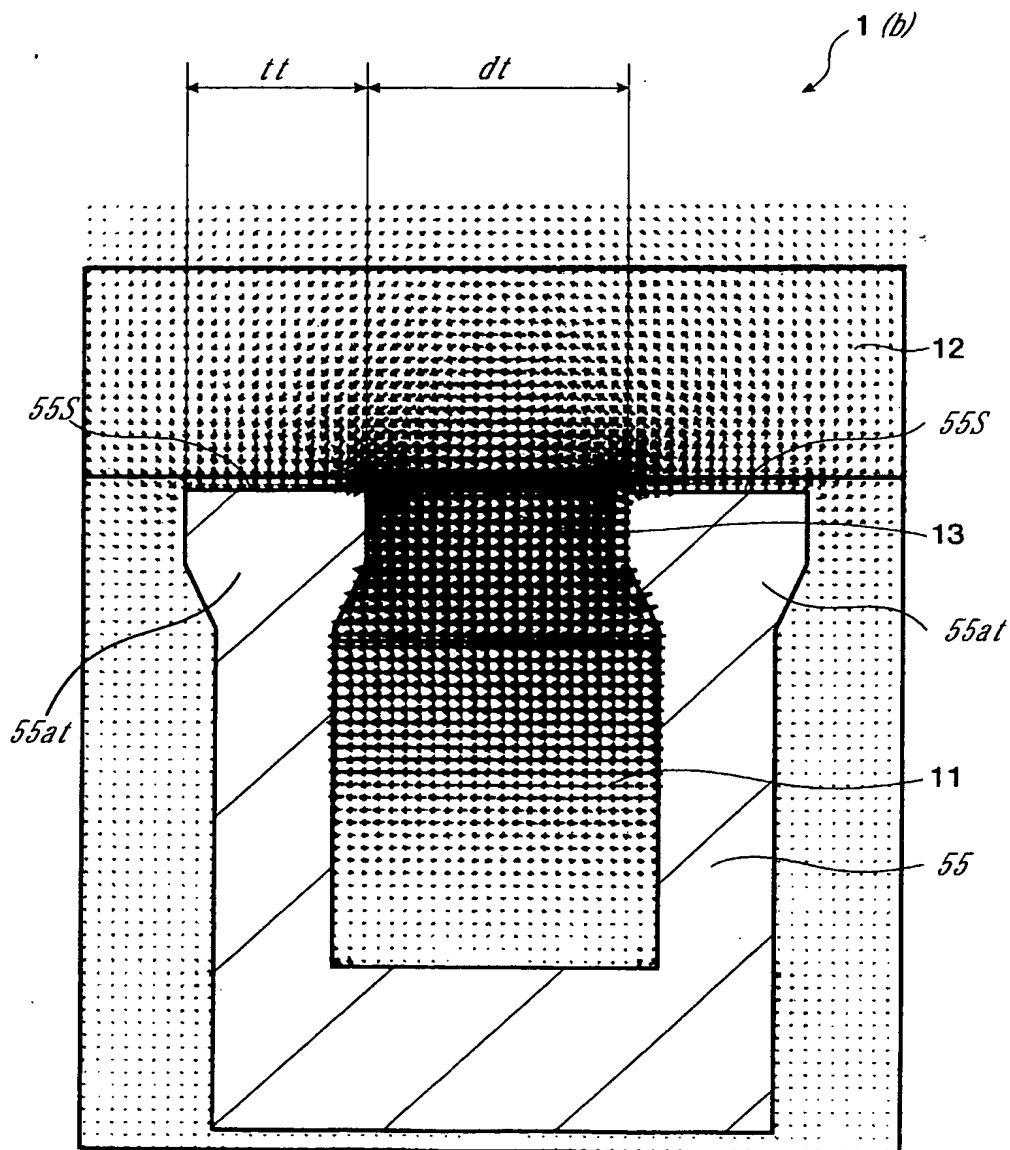


Fig.6

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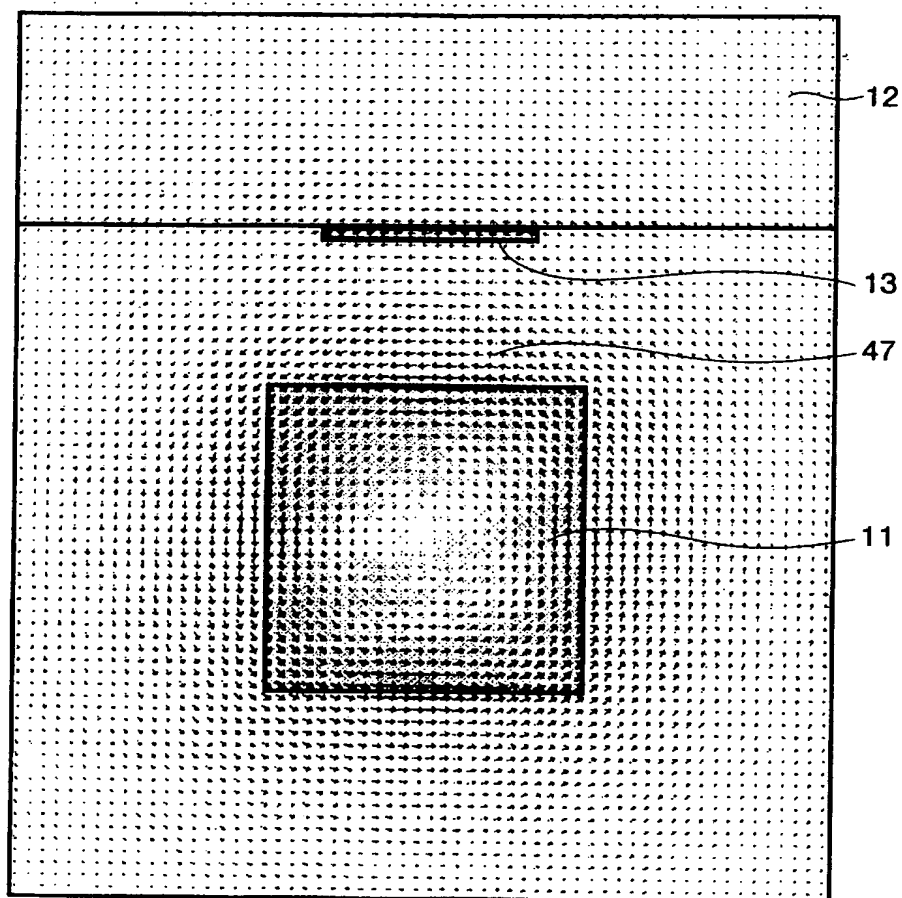


Fig.7

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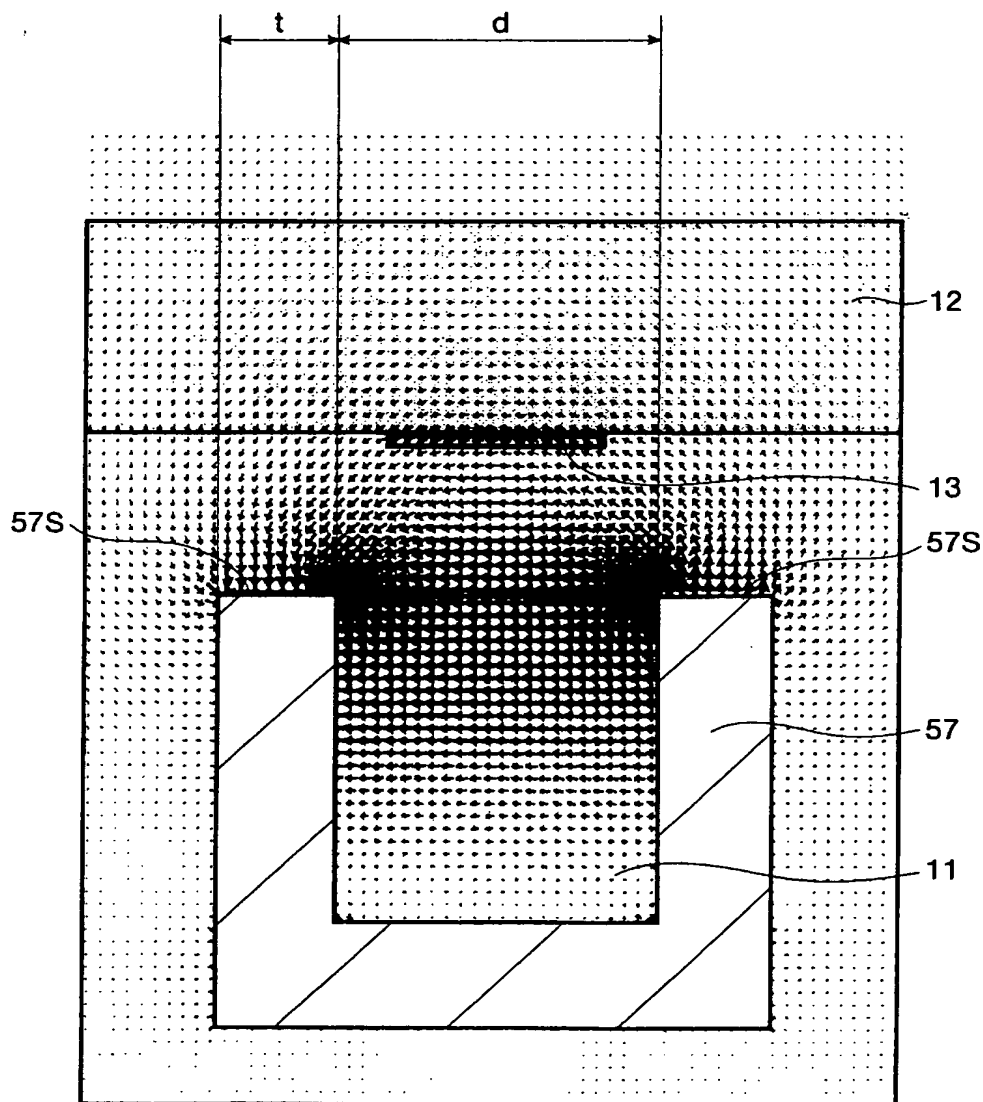


Fig.8

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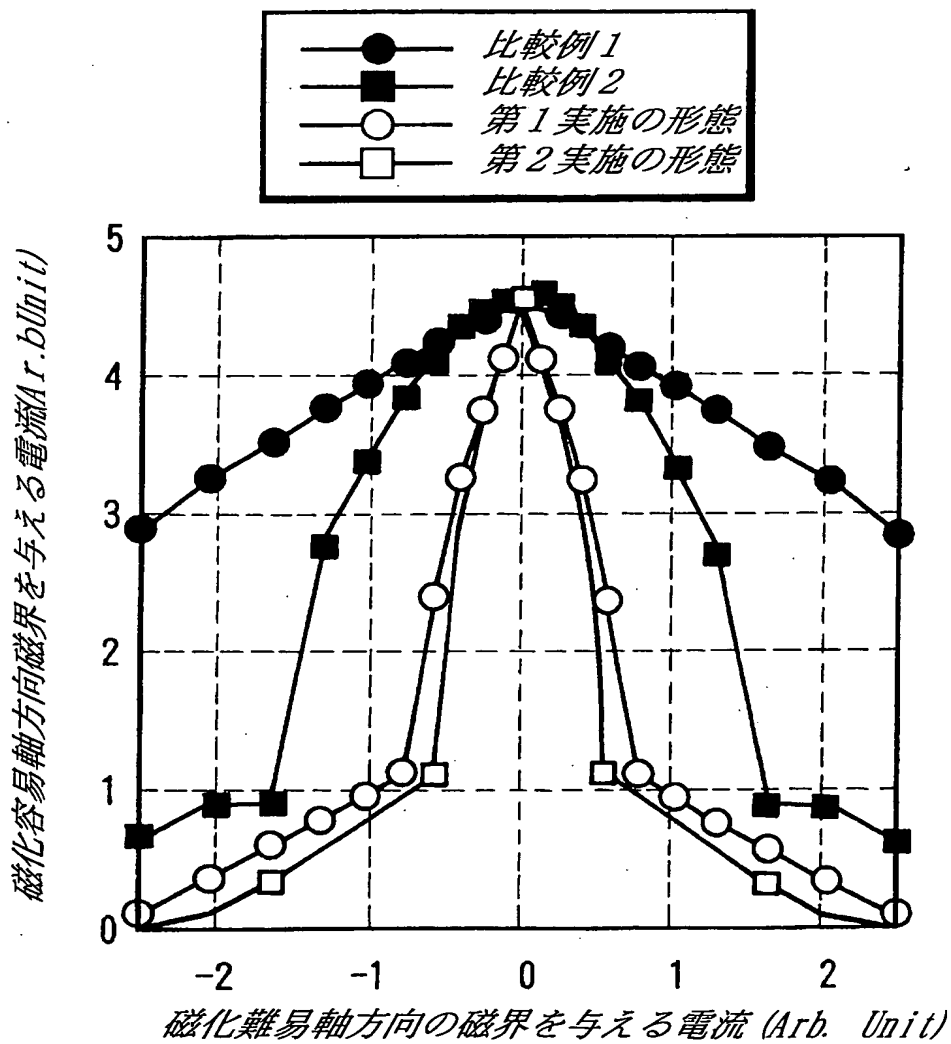


Fig.9



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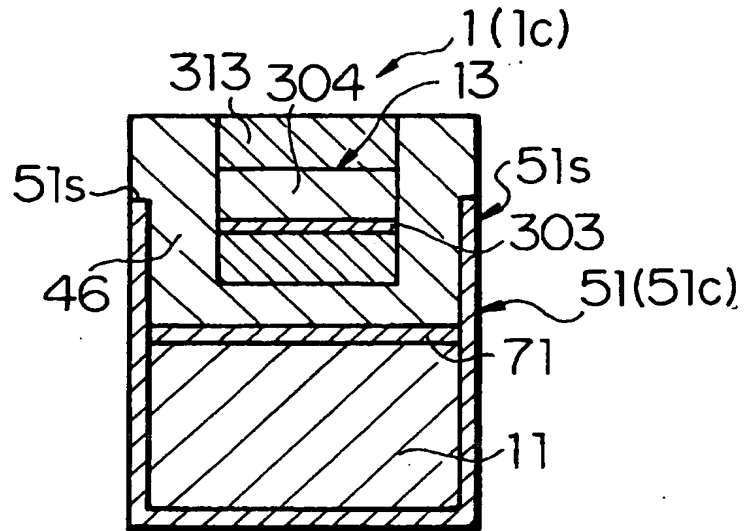


Fig.10

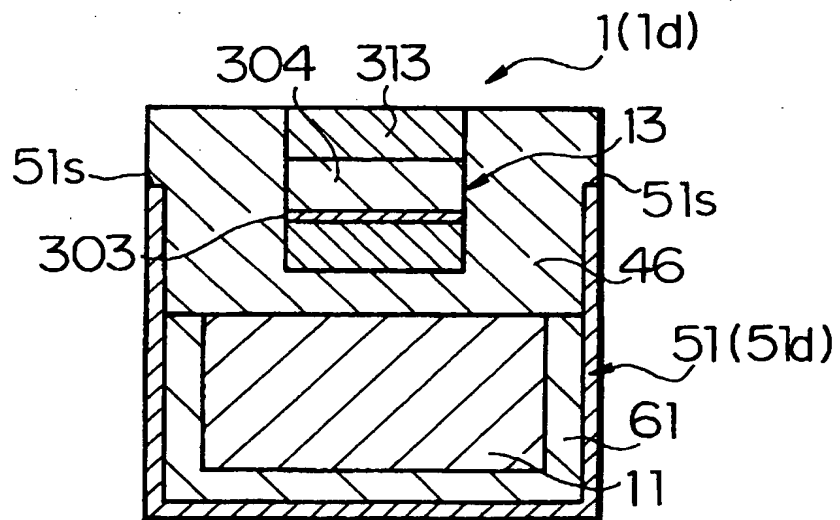


Fig.11

Fig. 1 is a cross-sectional view of a semiconductor device. The device includes a substrate 11 with a first conductive layer 13, a second conductive layer 46, and a third conductive layer 62. A central region 51 (51e) contains a stack of layers 303, 304, and 313. A side region 51s is also shown.

Fig.13

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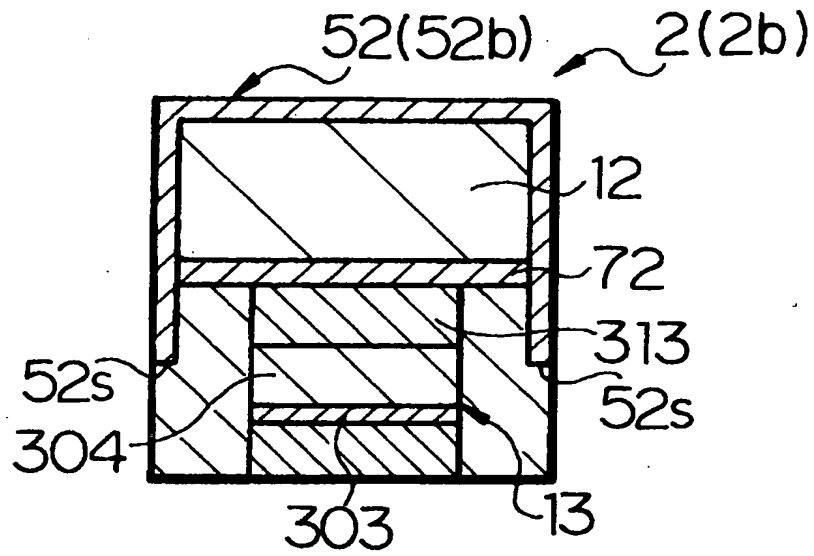


Fig.14

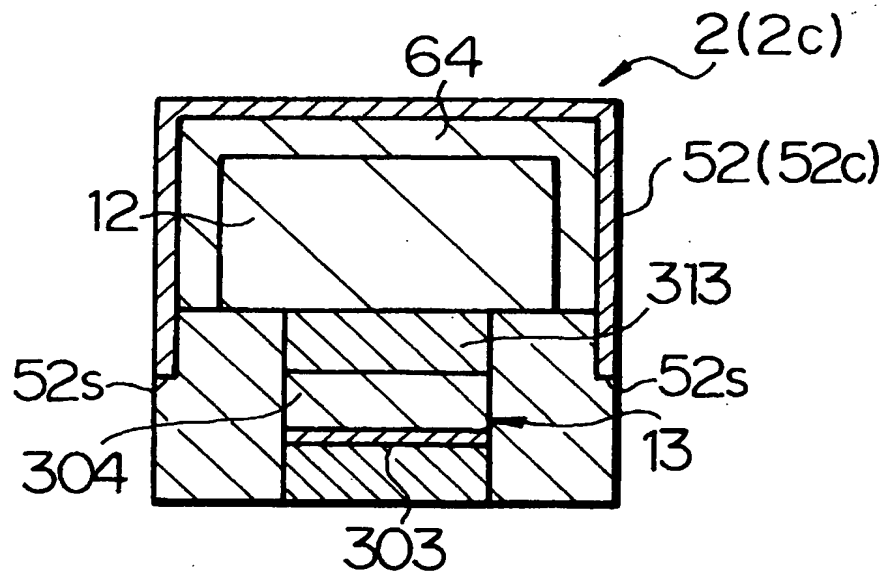


Fig.15

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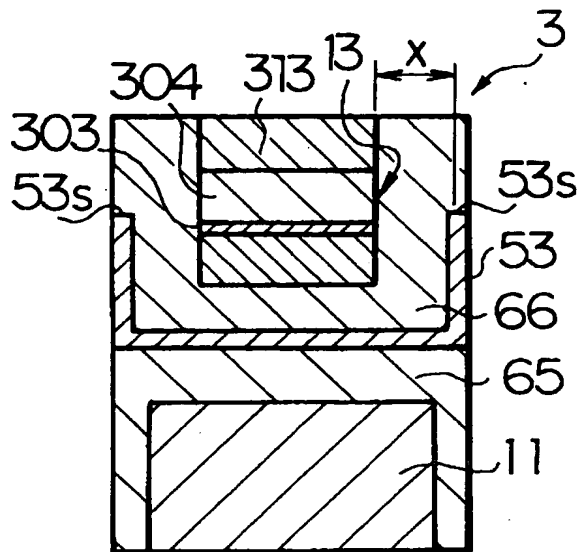


Fig.16

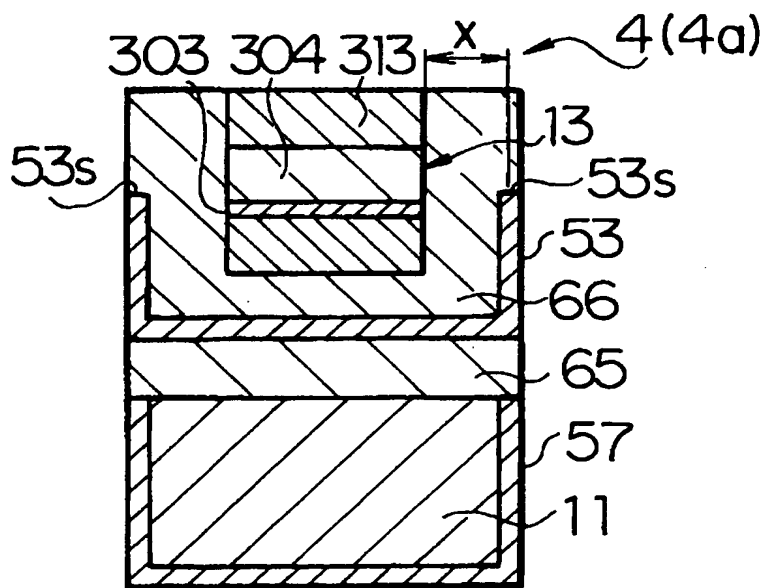


Fig.17

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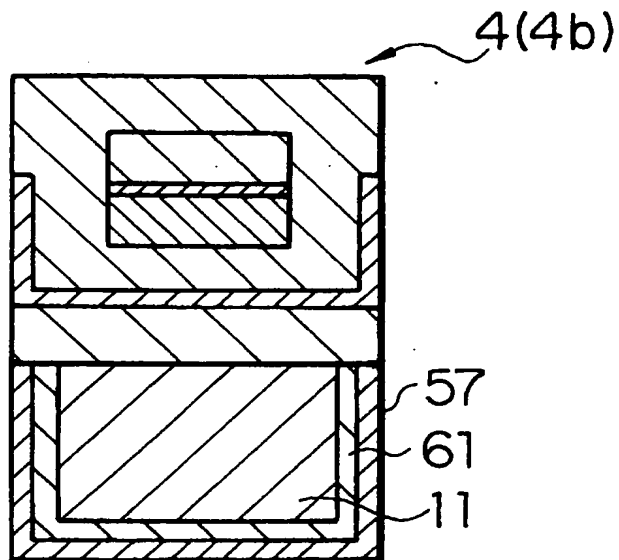


Fig.18

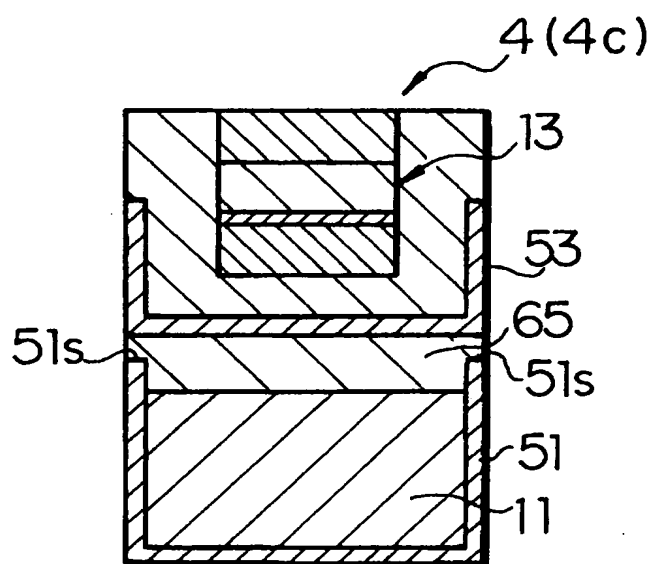


Fig.19

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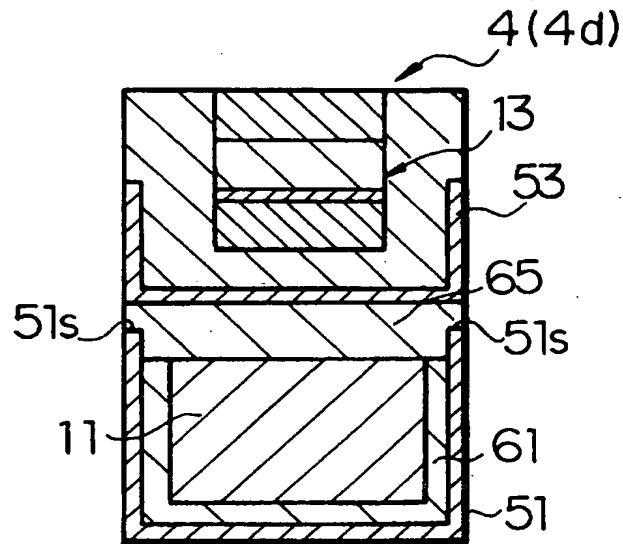


Fig.20

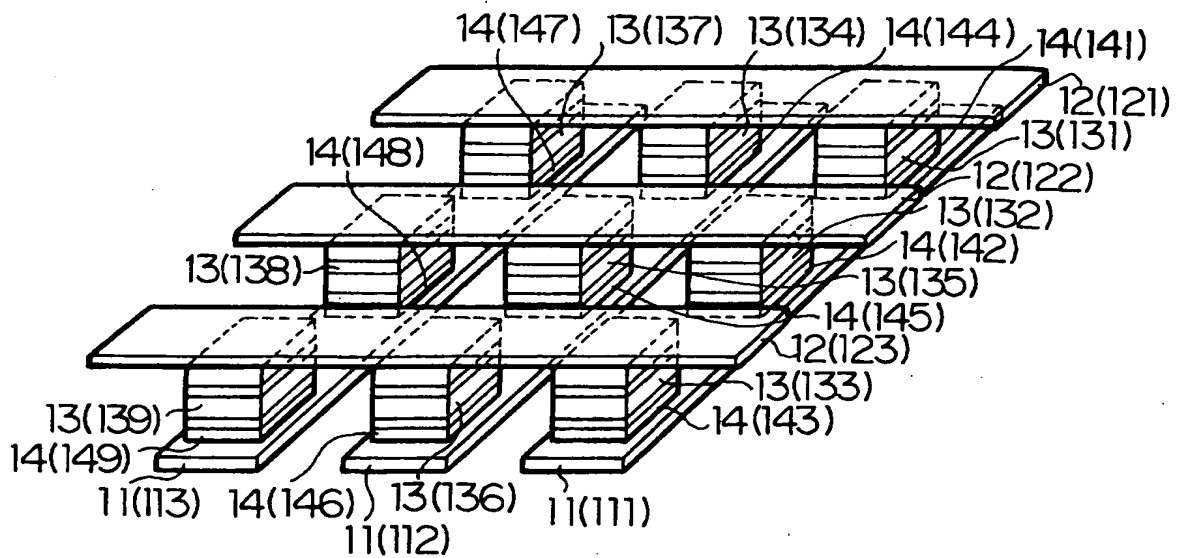


Fig.21

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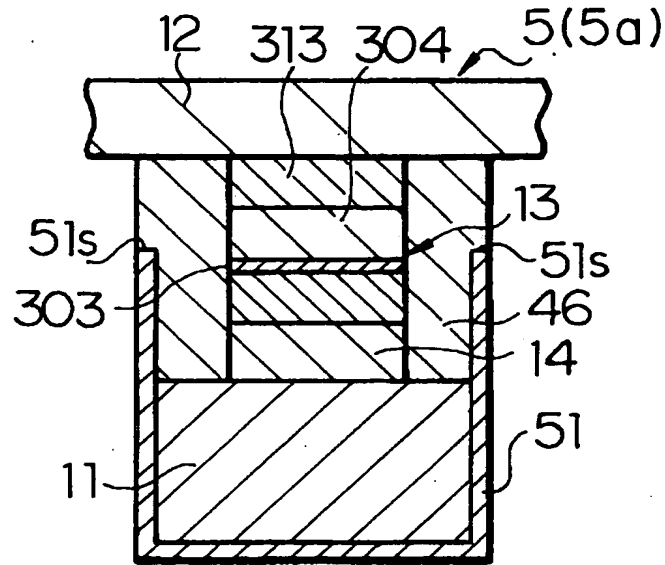


Fig.22

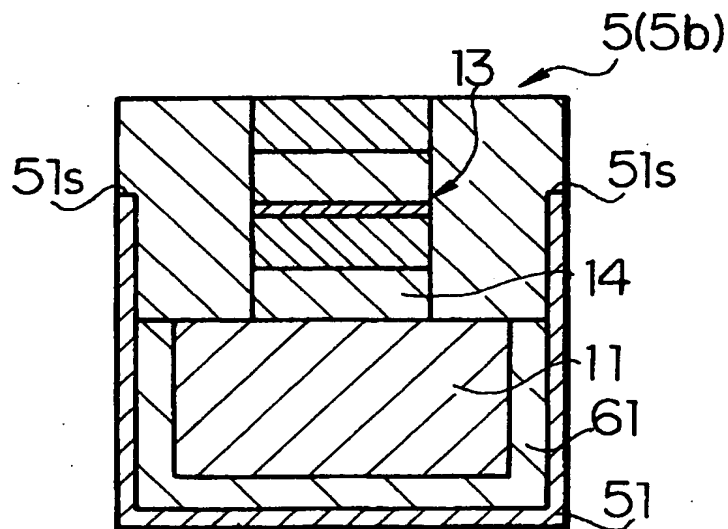


Fig.23

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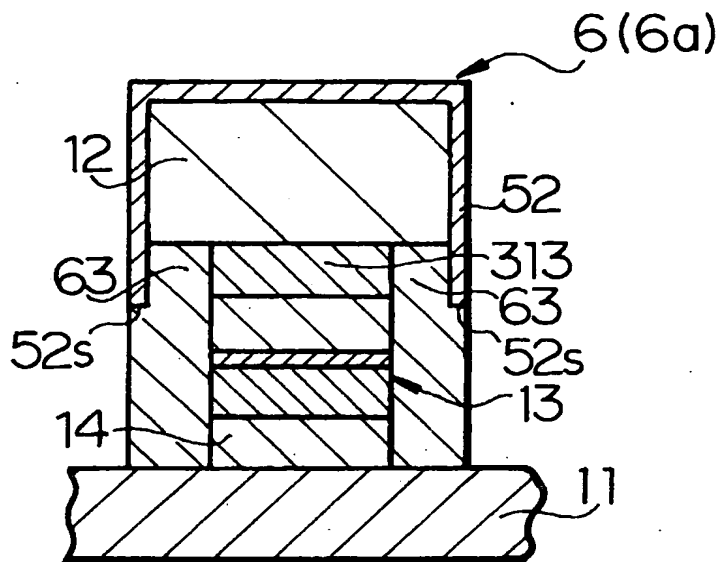


Fig.24

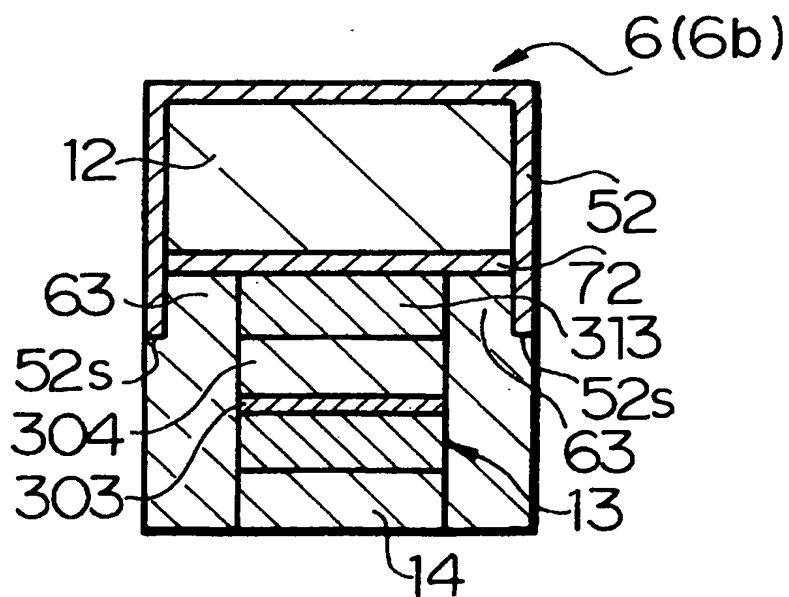


Fig.25



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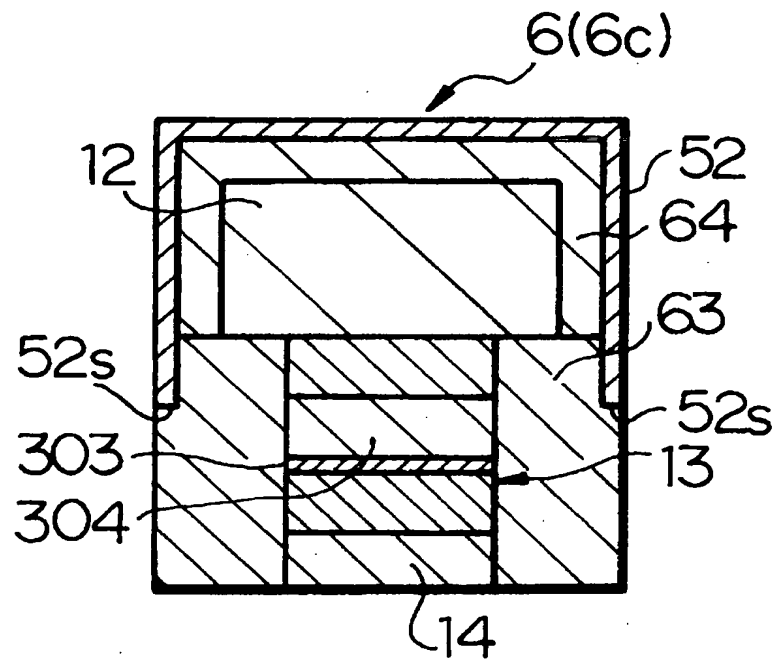
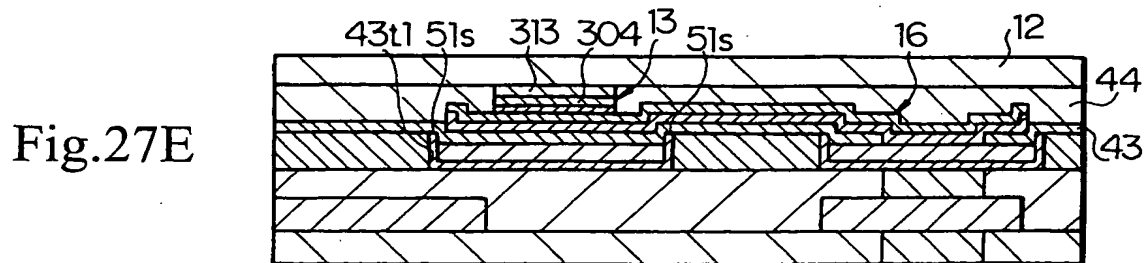
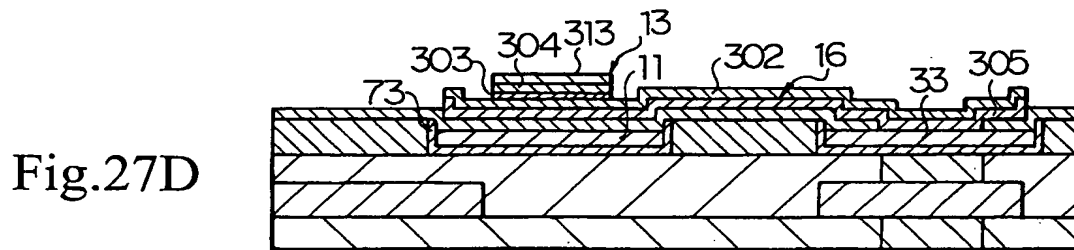
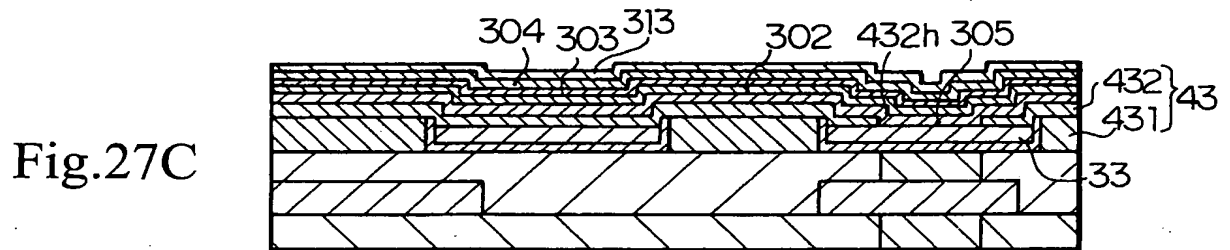
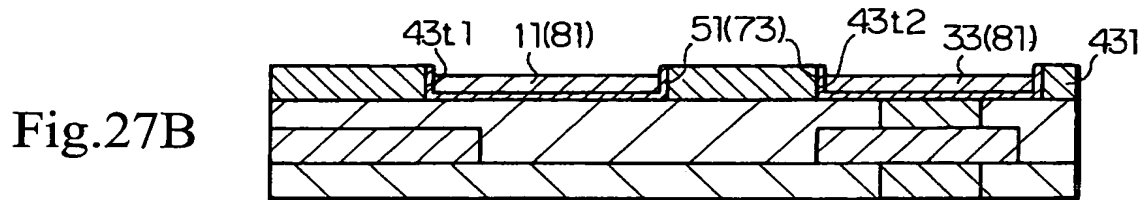
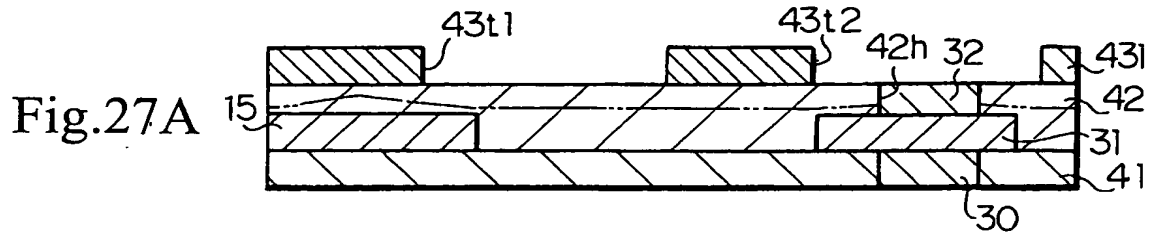
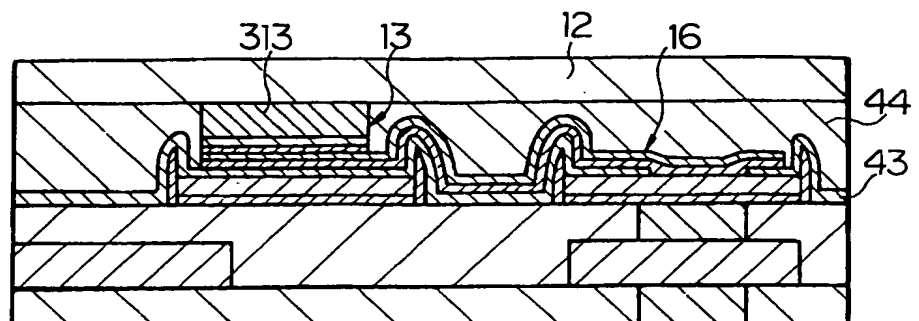


Fig.26

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Fig.29A

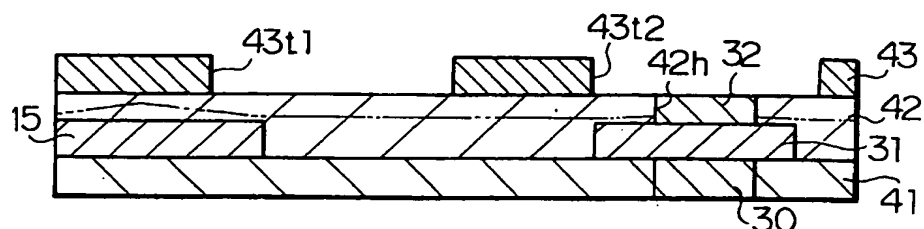


Fig.29B

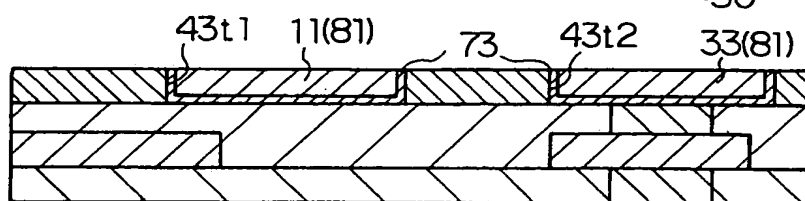


Fig.29C

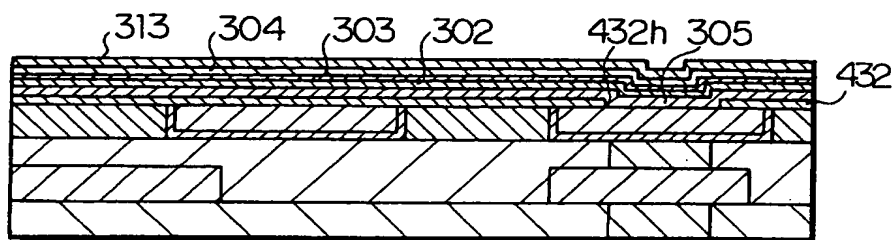


Fig.29D

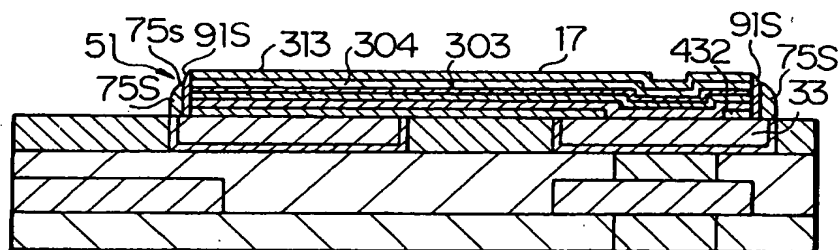


Fig.29E

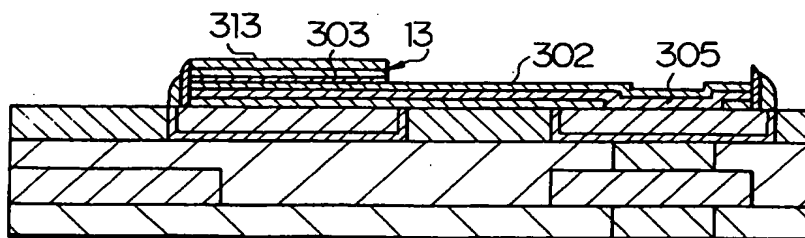
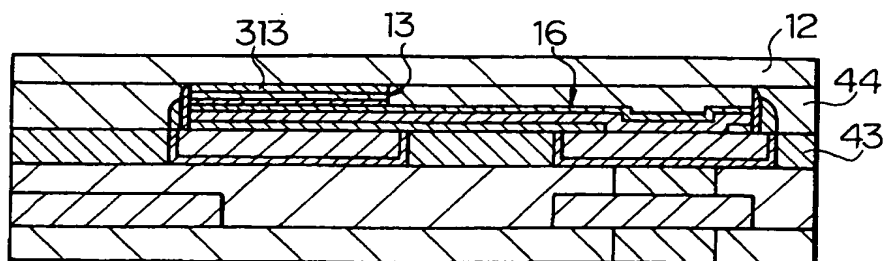


Fig.29F



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Fig.30A

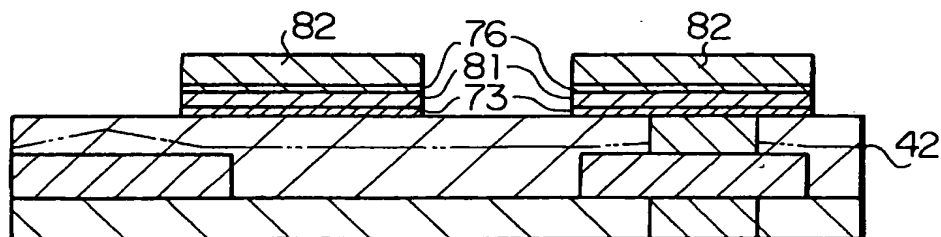
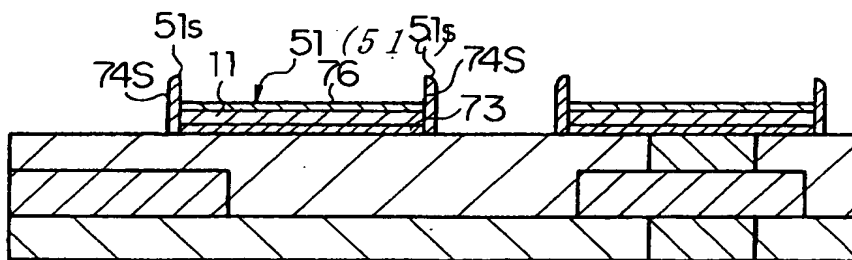


Fig.30B



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Fig.31A

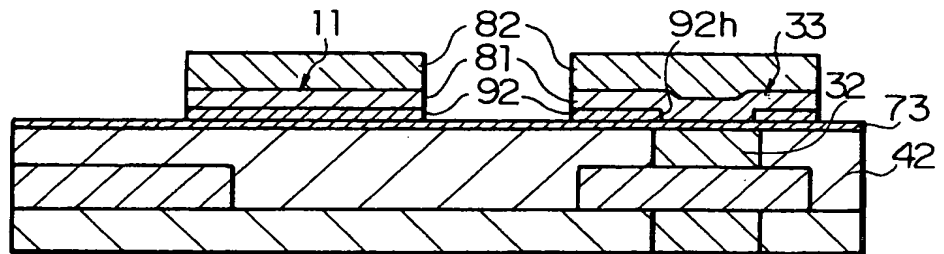
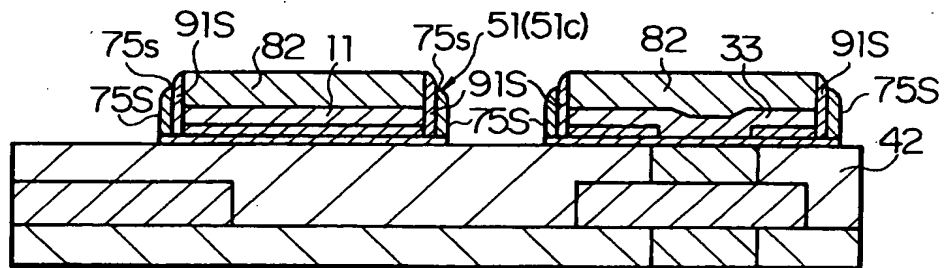


Fig.31B



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Fig.32A

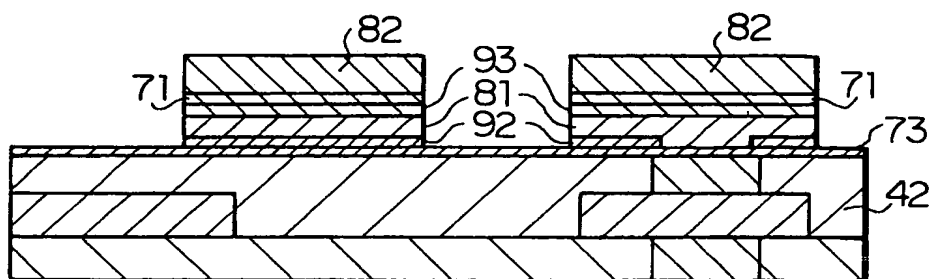
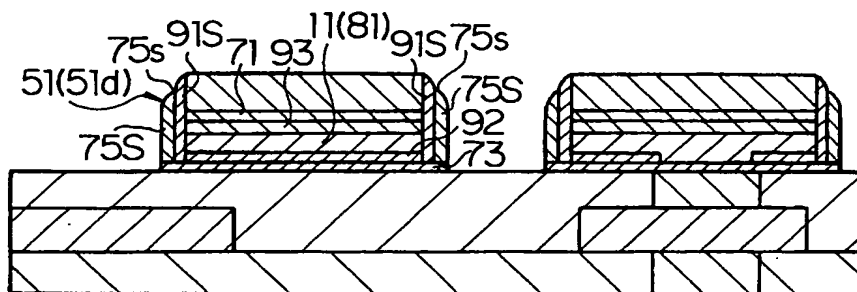


Fig.32B



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Fig.33A

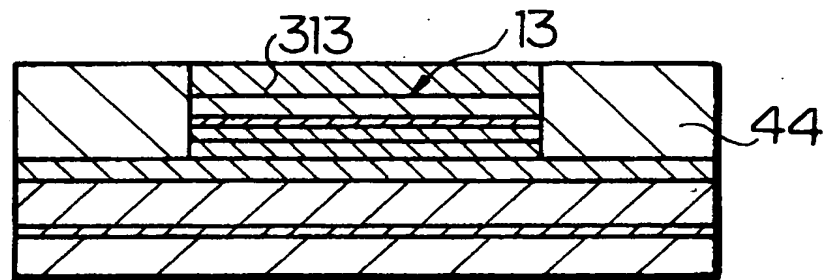


Fig.33B

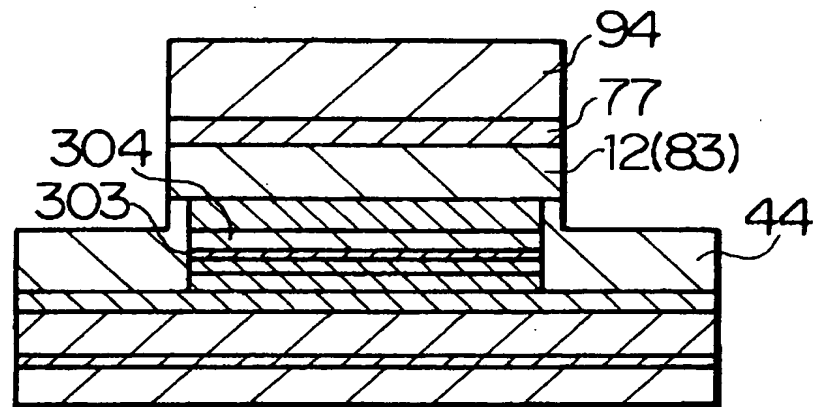
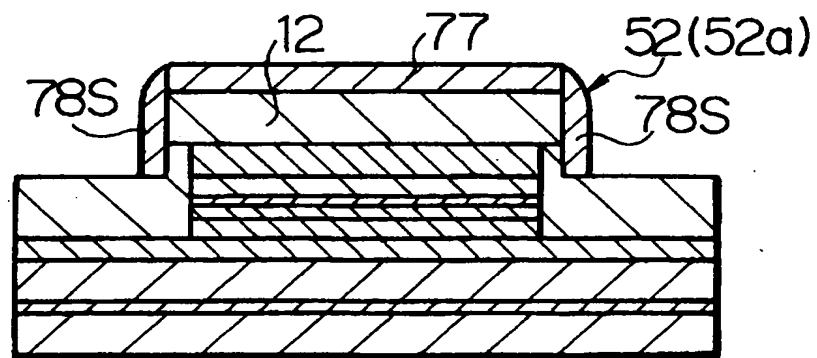


Fig.33C





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Fig.34A

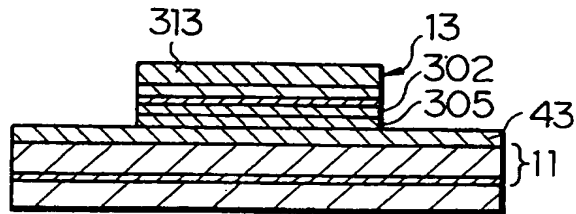


Fig.34B

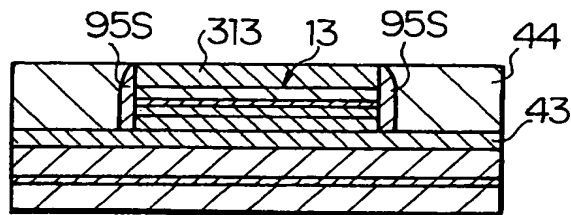


Fig.34C

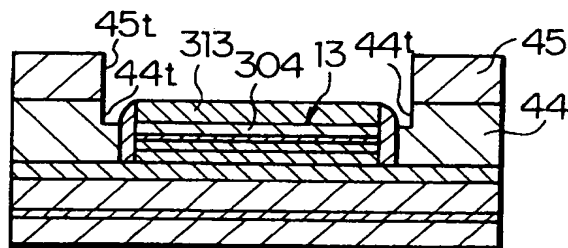


Fig.34D

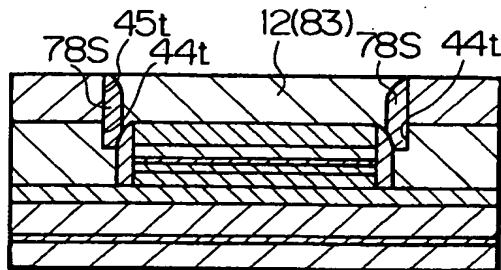
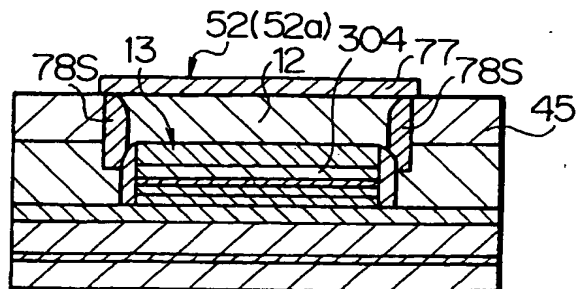


Fig.34E



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Fig.35A

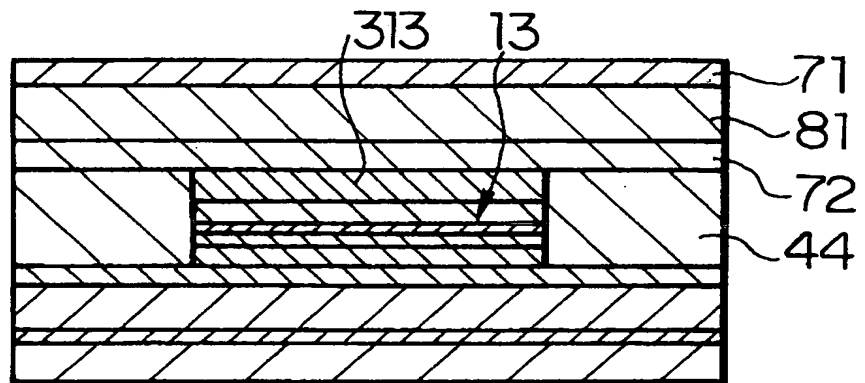
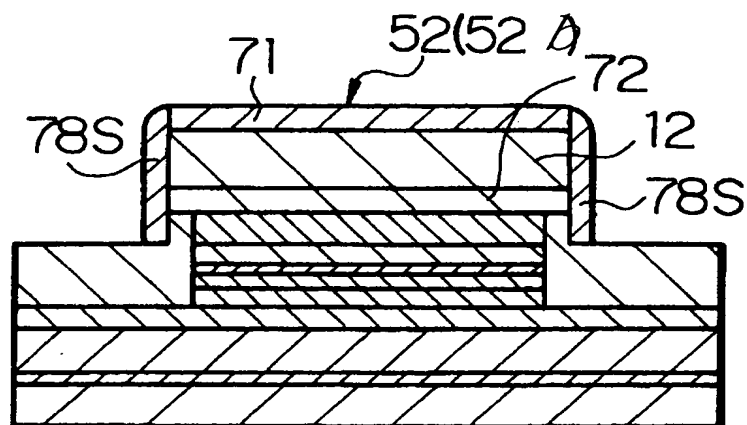


Fig.35B



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Fig.36A

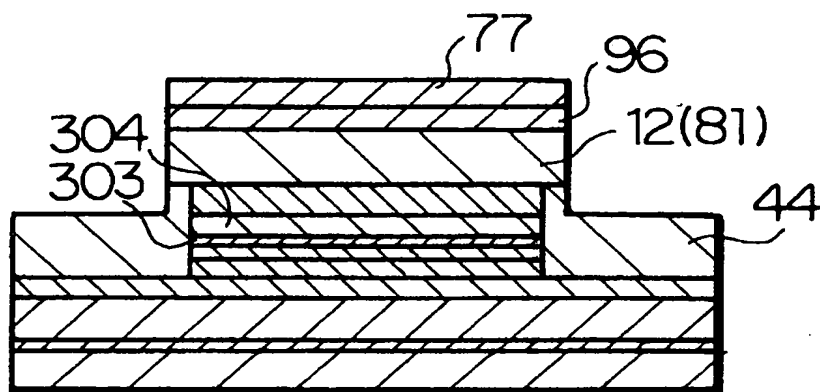
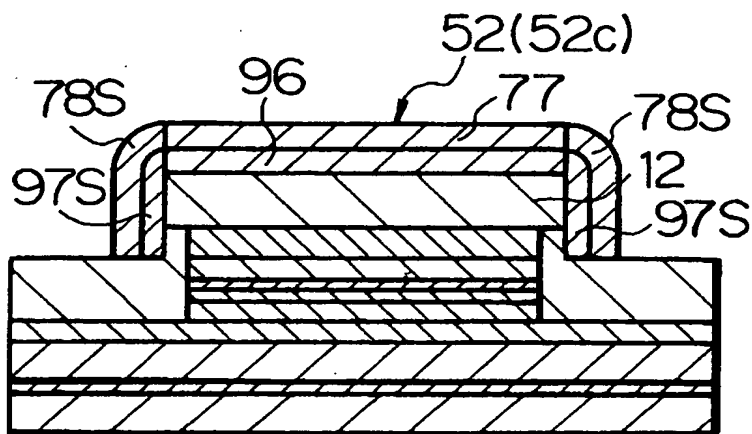


Fig.36B



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Fig.37A

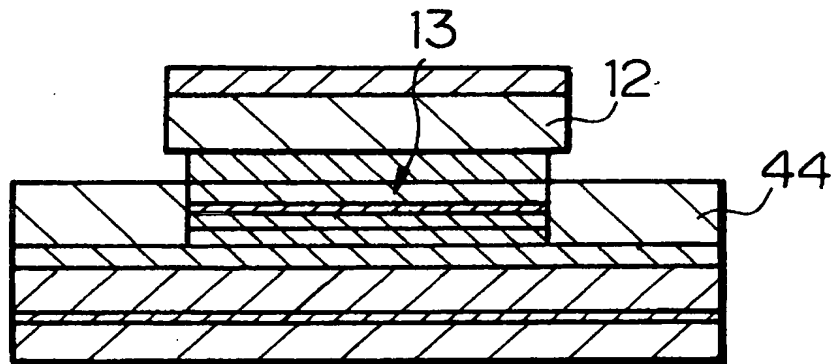
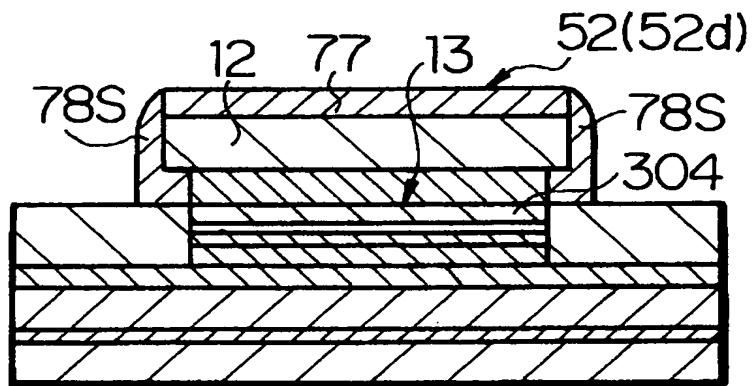


Fig.37B



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Fig.38A

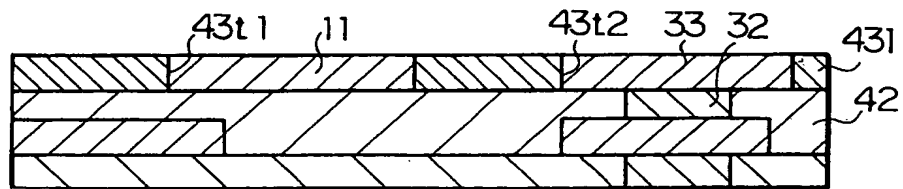


Fig.38B

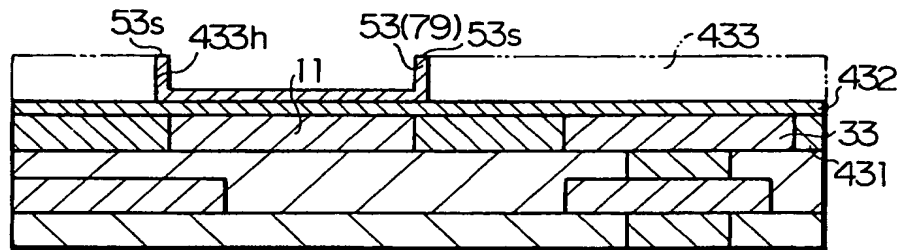
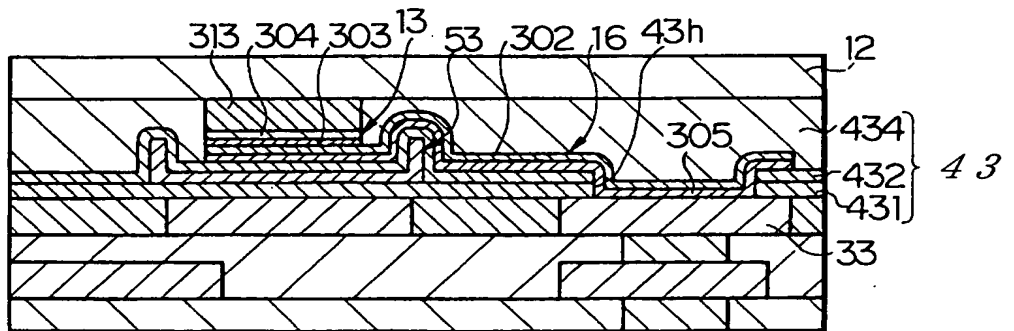


Fig.38C



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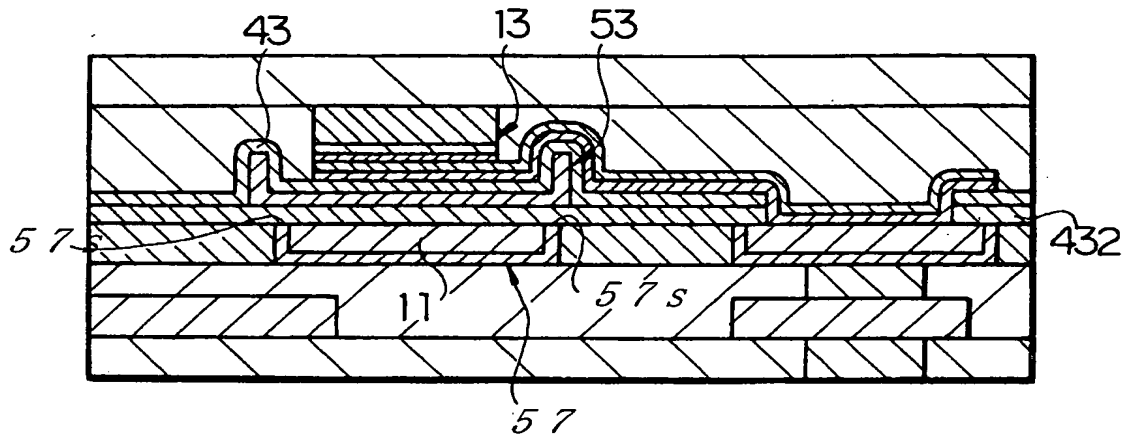


Fig.39

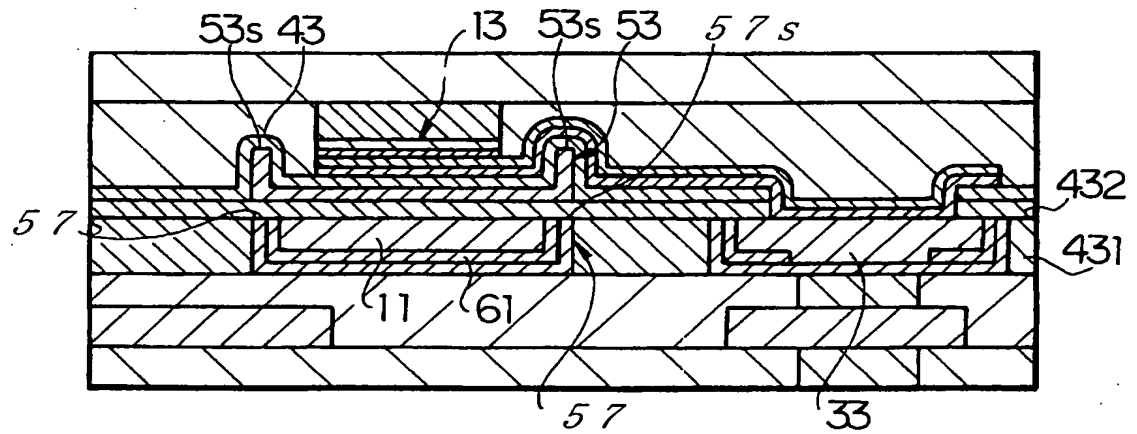


Fig.40

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Fig.41A

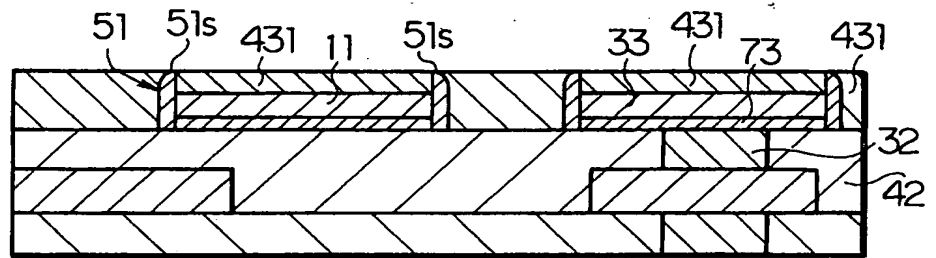
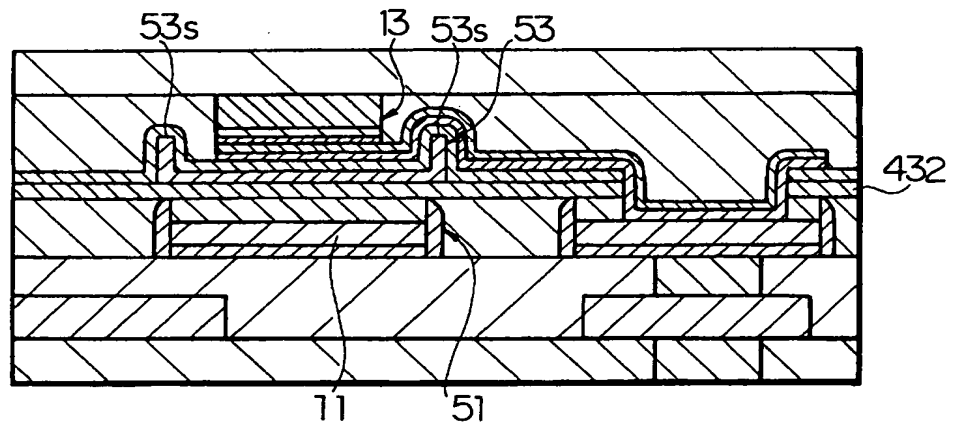


Fig.41B



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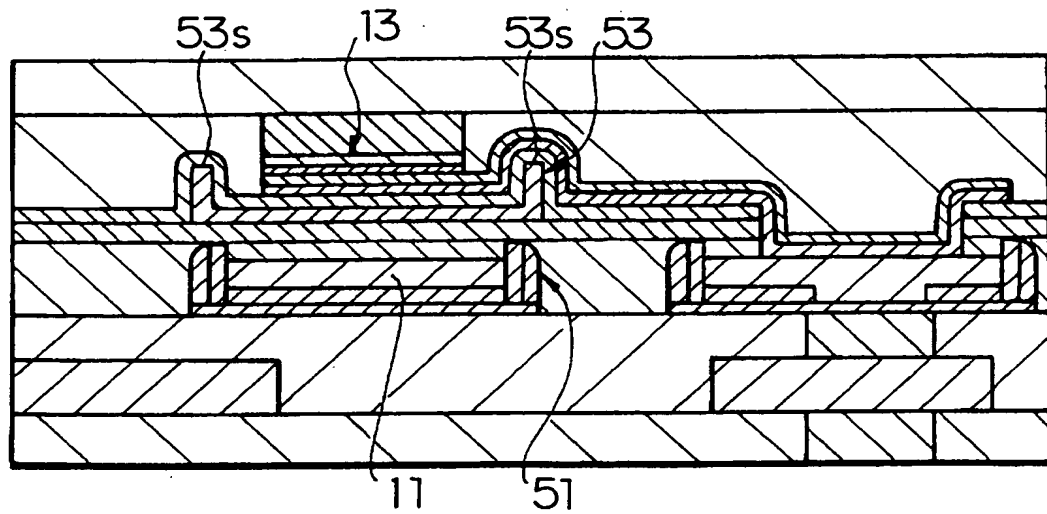


Fig.42



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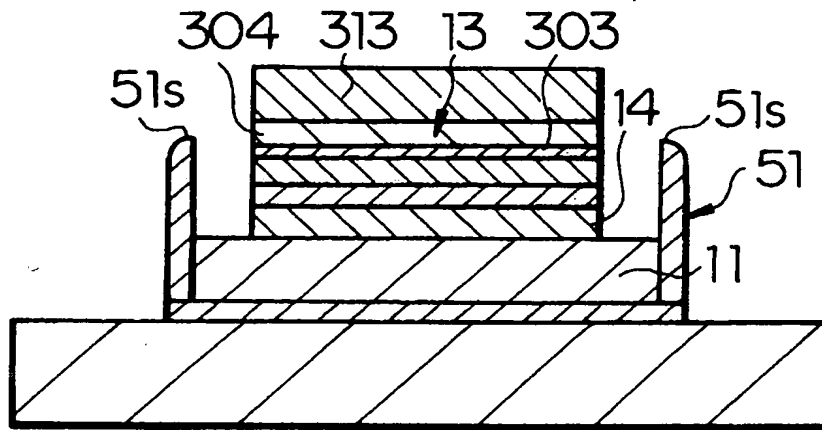


Fig.43

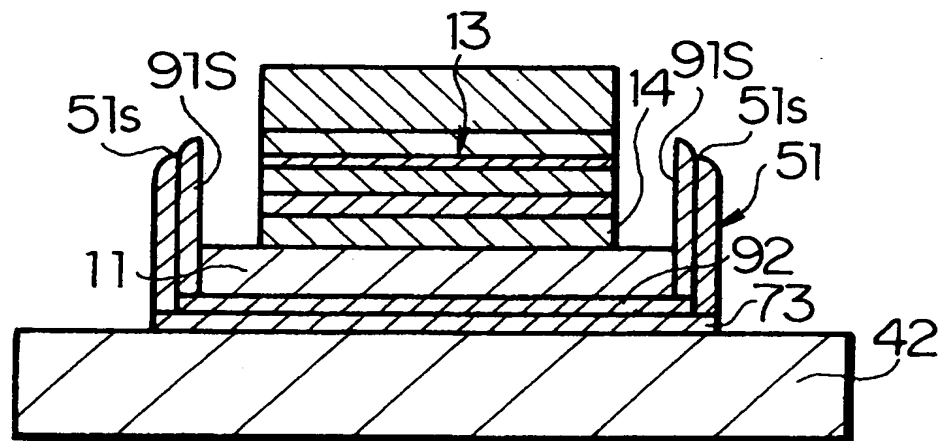


Fig.44